

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF: Walter Fix et al.

SERIAL NO: 10/562,869 GROUP ART UNIT: 2814

FILED: 04/07/2006 EXAMINER: Eva Montalvo

CUSTOMER NO.: 27162

FOR: LOGIC GATE WITH A POTENTIAL-FREE GATE ELECTRODE
FOR ORGANIC INTEGRATED CIRCUITS

ATTY/DKT NO.: 411000-144

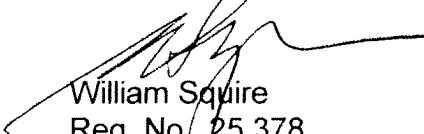
SUPPLEMENTARY IDS LETTER

SIR:

The abstract of the cited reference, JP 05152560, was previously cited in a prior Information Disclosure Statement. Applicants are now submitting an English translation of the entire document.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

Respectfully submitted,
Walter Fix et al.


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